Docket No.

246105US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Yoshio OZAWA, et al.

SERIAL NO: New Application

GAU:

FILED:

Herewith

EXAMINER:

FOR:

NONVOLATILE SEMICONDUCTOR MEMORY AND MANUFACTURING METHOD FOR THE SAME

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

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FOR: NONVOLATILE SEMICONDUCTOR MEMORY AND MANUFACTURING

METHOD FOR THE SAME

STATEMENT OF RELEVANCY

Reference AO (2001-168306) on Form PTO-1449:

A memory cell structure is shown that inter-poly dielectric layers are separated between two neighboring cells.

Form PTO 1449	U.S. DEPARTMENT OF COMMERCE			ATTY DOCKET NO.		SERIAL NO.		
(Modified)		PATENT AND TRAI	DEMARK OFFICE	246105US2		New Application		
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LIST OF	REFEF	RENCES CITED BY AP	PLICANT	Yoshio OZAWA, et al.				
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				U.S. PATENT DOCUMENTS				
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	AW	WH. LEE, et al., "A NOVEL HIGH K INTER-POLY DIELECTRIC (IPD), Al₂O₃ FOR LOW VOLTAGE/HIGH SPEED FLASH MEMORIES: ERASING IN MSECS AT 3.3V", 1997 Symposium on VLSI Technology Digest of Technical Papers, June 1997 Pages 117-118						
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	AZ				Add	itional Ref	erences sheet(s) attached	
Examiner					Date Co	Date Considered		
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